



## PATENT APPLICATION

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Nobuaki HASHIMOTO

Application No.: 09/486,561

Filed: February 29, 2000

SEMICONDUCTOR DEVICE AND METHOD THEREOF, CIRCUIT BOARD AND For: **ELECTRONIC INSTRUMENT** 

Group Art Unit: 2815

Examiner: L. Cruz

Docket No.: 105030

**AMENDMENT** 

Director of the U.S. Patent and Trademark Office Washington, D.C. 20231

Sir:

In reply to the Office Action mailed February 26, 2001, please amend the aboveidentified application as follows:

## IN THE DRAWINGS:

Please amend Figure 1D as set forth in the attached Request for Approval of Drawing Corrections.

## IN THE CLAIMS:

Please cancel claim 13 without prejudice to or disclaimer of the subject matter contained therein.

Please replace claims 11 and 14 as follows:

The semiconductor device as defined in claim 10, wherein said 11. (Amended) anisotropic conductive material is provided to cover said interconnect pattern in its entirety.

6-7-01, T. Floresc